

LORDS INSTITUTE OF ENGINEERING AND TECHNOLOGY

HIM AYAT SAGAR, HYDERABAD-500082

TIME TABLE

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING LIET/EEE/2010-2011/TT01

CURRICULLAM: R-07

ACADEMIC YEAR: 2010-2011

ROOM NO: 303

BATCH: 2007-2011 IV-I SEM TIME TABLE

W.E.F: 12-07-2010

	1 (9.30 AM- 10.20 AM)	2 (10.20 AM- 11.20 AM)	3 (11.20 AM- 12.10 PM)	4 (12.10 PM- 1.00 PM)	L U N C H	5 (1.30 PM- 2.15 PM)	6 (2.15 PM- 3.00 PM)	7 (3.00 PM- 3.45 PM)	8 (3.45 PM- 4.15 PM)
MON	NNFL	PSD	PSA	NCES		PSA	HVDC	PSOC	PSOC(T*)
TUE	PSOC	MPMC/EM LAB				NNFL	PSA	PSD	PSD(T*)
WED	PSA	MPMC/EM LAB				PSOC	NCES	HVDC	NNFL(T*)
THU	PSD	HVDC	PSOC	NNFL		NCES	REM**	PSOC	HVDC(T*)
FRI	NCES	PSD	REM**	G/L***			NNFL	HVDC	PSA(T*)
SAT	HVDC	NCES	REM**	PSA		PSD	NNFL	G/L***	NCES(T*)

T* - Tutorial Class

REM** - Remedial

G/L*** – Games & Library

Neural Networks & Fuzzy logic : B.ASHA KIRAN
 Power Semiconductor Drives : B.RAJESH
 Power System Analysis : NAGESWARA RAO
 Power System Operation & Control : B.BALAKRISHNA
 High Voltage DC Transmission : P.PRASUNA
 Non-Conventional Energy Sources : MRUDULA
 Micro Processor & Micro Controller lab : BAGHYA RAJU/VINAY CHOWDARY
 Electrical Measurements Lab : BALAKRISHNA/PANDU

Head of the Department

principal