

LORDS INSTITUTE OF ENGINEERING AND TECHNOLOGY

HIM AYAT SAGAR, HYDERABAD-500082

TIME TABLE

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING LIET/EEE/2010-2011/TT01

CURRICULLAM: R-07

ACADEMIC YEAR: 2010-2011

ROOM NO: 302

BATCH: 2008-2012 III-I SEM

W.E.F: 12-07-2010

	1 (9.30 AM-10.20 AM)	2 (10.20 AM-11.20 AM)	3 (11.20 AM-12.10 PM)	4 (12.10 PM-1.00 PM)	L U N C H	5 (1.30 PM-2.15 PM)	6 (2.15 PM-3.00 PM)	7 (3.00 PM-3.45 PM)	8 (3.45 PM-4.15 PM)
MON	EM-III	EM	CSO	LSA		PE	PS-II	G/L***	EM-III(T*)
TUE	PE	EM-III	EM	LSA		CS/EM-II LAB			LSA(T*)
WED	EM	EM-III	REM**	CSO		PE	PS-II	LSA	PE(T*)
THU	LSA	PS-II	CSO	EM		CS/EM-II LAB			PS-II(T*)
FRI	CSO	G/L***	PS-II	PE			EM-III	REM**	EM(T*)
SAT	PS-II	PE	EM	CSO		LSA	REM**	EM-III	CSO(T*)

T* - Tutorial Class

REM** - Remedial

G/L*** - Games & Library

Linear System Analysis	: B.BALAKRISHNA
Computer System Organization	: T.SRINIVAS
Power Electronics	: B.RAJESH
Electrical Measurements	: A.PANDU
Electrical Machines-III	: VIJAY KUMAR
Power Systems-II	: B.ASHA KIRAN
Electrical Machines-II Lab	: B.BALAKRISHNA / VIJAY KUMAR
Control Systems and Simulation Lab	: B.RAJESH/B.ASHA KIRAN

Class Adviser

Head of the Department

principal